

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	124	204/416.cior.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2008/07/29 09:34
L2	124	L1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2008/07/29 09:34
L3	1269	ISFET OR (ion-selective field effective transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2008/07/29 09:34
L4	5	L3 and pass transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2008/07/29 09:34
L5	5	L4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2008/07/29 09:34
L6	107	L3 and signal process\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2008/07/29 09:34
L7	107	L6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2008/07/29 09:34
L8	10	L3 and signal condition\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2008/07/29 09:34
L9	10	L8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2008/07/29 09:34

L10	340	L3 and (between SAME (input or source) SAME (output or drain))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2008/07/29 09:34
L11	340	L10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2008/07/29 09:34
L12	23	L3 and (threshold SAME between SAME (input or source) SAME (output or drain))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2008/07/29 09:34
L13	23	L12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2008/07/29 09:34
L14	423	switching (device OR control\$3) AND sensor array	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2008/07/29 09:35
L15	423	L14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2008/07/29 09:35
L16	0	L14 and ion-selective field effect transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2008/07/29 09:35
L17	0	L16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2008/07/29 09:35
L18	2	L14 and ISFET	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2008/07/29 09:35
L19	2	L18	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	ADJ	OFF	2008/07/29 09:35

7/29/2008 9:35:28 AM

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